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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/814,183	04/01/2004	Takehiro Yoshida	2004_0504A	6452

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WENDEROTH, LIND & PONACK, L.L.P.
2033 K STREET N. W.
SUITE 800
WASHINGTON, DC 20006-1021

EXAMINER	
KANG, INSUN	

ART UNIT	PAPER NUMBER
2193	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/814,183	YOSHIDA ET AL.	
	Examiner	Art Unit	
	INSUN KANG	2193	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *9/17/2007 and 12/26/2007*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-3 and 6-10* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-3 and 6-10* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *26 December 2007* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *9/17/2007*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

1. This action is in response to the amendment filed on 9/17/2007 and 12/26/2007.

2. As per applicant's request, claims 4 and 5 have been cancelled, claims 1-3 and 6-10 have been amended. Claims 1-3 and 6-10 are pending in the application.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claims 1-3 and 6-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ishiwata (US Pub.No. 2002/0016957 published on 2/7/2002) in view of Fetzer et al. (US Patent 6,832,302) hereafter Fetzer.

Per claim 1:

Ishiwata discloses:

- A program linking program, which causes a computer having a memory to function as: linking means, to link one or a plurality among plural unlinked programs, advancing toward the completion of one or more linked programs (i.e. "A linker starting station 12 starts a liner 17 based on the linking order held by the linking order section 11a and causes it to execute the linking process, and thus an executable object 18 is formed," page 4, 0086)

- storage means, to cause the memory to store the one or more linked programs, either before or after completion (i.e. "the storing section 14 stores the minimum program size out of the formed executable objects 18 by repeating respective steps by a repeating section 15," page 4, 0090);
- and, management means, to cause the linking means to preferentially perform linking of the plural unlinked programs in predetermined priority order and to a maximum limit (i.e. "The linking order forming section 51 is a unit that forms the linking order of the intermediate objects 56," page 7, 0161; "the minimum program size and the linking order of the intermediate objects 56 used when the executable object 58 having this minimum program size is obtained are stored in the storing section 54," page 7, 0165).

Ishiwata discloses obtaining the minimum program size by the linker order forming section but does not explicitly teach ensuring linking within a range in which overflow of a predetermined capacity of the memory does not occur. However, Fetzer teaches such a buffer overflow check was known in the pertinent art, at the time applicant's invention was made, to ensure sufficient memory space to accommodate data storage (i.e. col. 3 lines 50-60). It would have been obvious for one having ordinary skill in the art to modify Ishiwata's disclosed system to incorporate the teachings of Fetzer. The modification would be obvious because one having ordinary skill in the art would be motivated to perform a memory boundary check to prevent buffer overrun that can cause potential memory fault as suggested by Fetzer (i.e. col. 3 lines 50-60).

Ishiwata further discloses:

-the predetermined priority order is selected from at least one of: increasing order of frequency of use of each of the plurality of unlinked programs to create the plurality of linked programs; increasing order of size of each of the plurality of unlinked programs such that a program size of each of the plurality of linked programs is not always reduced; increasing order of product of frequency of use of each of the plurality of unlinked programs to create the plurality of linked programs and a size of a corresponding one of the plurality of unlinked programs; decreasing order of time for linking each of the plurality of unlinked programs upon execution; and decreasing order of execution frequency of each of the plurality of unlinked programs accompanying execution of the plurality of linked programs (i.e. "based on one genes of a predetermined number are formed first by the linking order forming section 11 to get the executable object 18…the program size of the executable object 18…the minimum value…of the program size," page 5, 0124-0126).

Per claim 2:

Ishiwata further discloses:

- wherein the management means causes the linking means to perform linking, and as a result determine the maximum limit(i.e. "The linking order forming section 51 is a unit that forms the linking order of the intermediate objects 56," page 7, 0161; "the minimum program size and the linking order of the intermediate objects 56 used when the executable object 58 having this minimum program size is obtained are stored in the storing section 54," page 7, 0165).

Per claim 3:

Ishiwata further discloses:

- wherein the management means determines the maximum limit by evaluating the sizes of the one or more linked programs at each stage of linking, without causing the linking means to perform linking (i.e. "a comparing step which compares program size of the executable objects obtained by the linking processing step with the program size of a executable objects stored in a storing section every time when the liking order is changed," page 1, 0014).

Per claim 6, it is the program linking program version of claim 1, respectively, and is rejected for the same reasons set forth in connection with the rejection of claim 1 above.

Per claim 7, it is the program linking device version of claim 1, respectively, and is rejected for the same reasons set forth in connection with the rejection of claim 1 above.

Per claim 8:

Ishiwata discloses:

- a memory (i.e. memory," page 1, 0004)
- a linking unit, to link one or a plurality among plural unlinked programs, advancing toward the completion of one or more linked programs (i.e. "A linker starting station 12 starts a liner 17 based on the linking order held by the linking order section 11a and causes it to execute the linking process, and thus an executable object 18 is formed," page 4, 0086)

- a storage unit, to cause the memory to store the one or more linked programs, either before or after completion (i.e. "the storing section 14 stores the minimum program size out of the formed executable objects 18 by repeating respective steps by a repeating section 15," page 4, 0090);
- a management unit, to cause the linking unit to preferentially perform linking of the plural unlinked programs in predetermined priority order and to a maximum limit (i.e. "The linking order forming section 51 is a unit that forms the linking order of the intermediate objects 56," page 7, 0161; "the minimum program size and the linking order of the intermediate objects 56 used when the executable object 58 having this minimum program size is obtained are stored in the storing section 54," page 7, 0165).

Ishiwata discloses obtaining the minimum program size by the linker order forming section but does not explicitly teach ensuring linking within a range in which overflow of a predetermined capacity of the memory does not occur. However, Fetzer teaches such a buffer overflow check was known in the pertinent art, at the time applicant's invention was made, to ensure sufficient memory space to accommodate data storage (i.e. col. 3 lines 50-60). It would have been obvious for one having ordinary skill in the art to modify Ishiwata's disclosed system to incorporate the teachings of Fetzer. The modification would be obvious because one having ordinary skill in the art would be motivated to perform a memory boundary check to prevent buffer overrun that can cause potential memory fault as suggested by Fetzer (i.e. col. 3 lines 50-60).

Ishiwata further discloses:

- an execution control unit, to execute, among the one or more linked programs stored in the memory, a designated program; and wherein the execution control unit has runtime linking unit that, when a linked program to be executed is not completed as regards linking, completes the linked program to be executed by linking one or a plurality of programs from among the plural unlinked programs (i.e. "a repeating step for changing the linking orders by the intermediate object liking order forming step and executing repeatedly the linking processing step, the comparing step, and the storing step," page 3, 0069; 0086; 0052;0016).

Ishiwata further discloses:

-the predetermined priority order is selected from at least one of: increasing order of frequency of use of each of the plurality of unlinked programs to create the plurality of linked programs; increasing order of size of each of the plurality of unlinked programs such that a program size of each of the plurality of linked programs is not always reduced; increasing order of product of frequency of use of each of the plurality of unlinked programs to create the plurality of linked programs and a size of a corresponding one of the plurality of unlinked programs; decreasing order of time for linking each of the plurality of unlinked programs upon execution; and decreasing order of execution frequency of each of the plurality of unlinked programs accompanying execution of the plurality of linked programs (i.e. "based on one genes of a predetermined number are formed first by the linking order forming section 11 to get the executable object 18…the program size of the executable object 18…the minimum value…of the program size," page 5, 0124-0126).

Per claim 9:

Ishiwata further discloses:

- an acquisition unit to acquire the plurality of unlinked programs, and a storing unit operable to store the plurality of unlinked programs acquired by the acquisition unit (i.e. "The intermediate object linking unit 10 comprises a linking order forming section 11…a linker starting section 12…a storing section," page 4 0085).

Per claim 10, it is the method version of claim 1, respectively, and is rejected for the same reasons set forth in connection with the rejection of claim 1 above.

Response to Arguments

5. Applicant's arguments filed on 12/26/2007 have been fully considered but they are not persuasive.

The applicant states that Ishiwata does not disclose increasing order of size…such that a program size of each of the plurality of linked program is not always reduced.

In response, it is noted that the limitation is recited: wherein the predetermined priority order is selected from at least one of. The claims are not recited as: "selected from all of": increasing order of frequency…increasing order of size…increasing order of product…decreasing order of time etc. Furthermore, Ishiwata discloses forming the linking order in a minimum program size from the linking order section 11a (0125) to obtain fast execution. The linking order is selected from decreasing order of execution time in Ishiwata (0156). Therefore, Ishiwata discloses one of the claimed predetermined priority order in the instant claims.

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to INSUN KANG whose telephone number is (571)272-3724. The examiner can normally be reached on M-F 8:30-5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis A. Bullock, Jr. can be reached on 571-272-3759. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-

direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Insun Kang/
Examiner, Art Unit 2193

/Lewis A. Bullock, Jr./
Supervisory Patent Examiner, Art Unit 2193